



Real-time Vision Algorithm Implementation and Application

A Special Track of the 4th International Symposium on
Visual Computing (ISVC08) www.isvc.net

Scope:

Processing speed is critical for many visual computing tasks. Many algorithms generate accurate results but run too slowly to produce results in real-time. On the other hand, some algorithms process at camera frame rates but with reduced accuracy, a more useful combination for real-time applications. This special track is a forum for research related to implementations of vision algorithms suitable for real-time applications.

Topics:

The topics of interest include but are not limited to the following areas:

- FPGA-based hardware acceleration of vision algorithms
- GPU-based acceleration of vision algorithms
- Optimized software-based implementation of vision algorithms
- Tradeoff analysis between processing speed and accuracy
- Visual computing for small unmanned vehicle navigation and obstacle avoidance
- Visual computing for real-time surveillance and monitoring applications
- Machine vision applications that require real-time performance

Paper Submission Procedure:

Papers submitted to any ISVC 2008 Special Track must not have been previously published and must not be currently under consideration for publication elsewhere. Manuscripts should be submitted in camera-ready format and should not exceed **12 pages**, including figures and tables (see <http://www.isvc.net> for details).

All papers accepted will appear in the symposium proceedings which will be published by **Springer-Verlag** in the **Lecture Notes in Computer Science (LNCS)** series.



Important Dates:

Paper submissions	July 21, 2008
Notification of acceptance	September 1, 2008
Final camera ready paper	September 15, 2008
Advance Registration	September 15, 2008
ISVC08 Symposium	December 1-3, 2008

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